

# Powered Device PowerDsine's Compatibility Report



ChipPC JackPC June 30, 2006



# 1. Vendor Details

This section is to be filled by the PD vendor.

PD Details	
Vendor Name:	ChipPC
Website address:	http://www.chippc.com/
Model name:	JackPC
PD type:	Thin client
Description:	
Part Number:	EFI 6800
Serial Number:	
Date:	June 30, 2006
Tested by:	Pavlick Rimboim

# 2. Test Setup

The compatibility test setup includes several items described hereafter. All these items are tested with the following configurations:

- 1. **Midspan** PD is connected to the output of PowerDsine's Midspans series (figure 1-a).
- PoE Enabled Switch –PD is connected to the output of a PoE switch, based on PowerDsine's integrated solutions (figure 1-b).

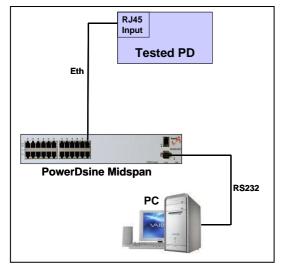


Figure 1-a - Midspan test setup

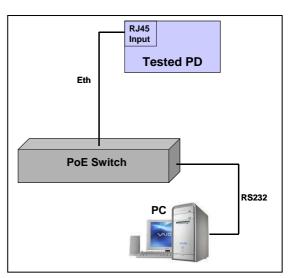


Figure 1-b - PoE switch test setup

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# **International Headquarters**PowerDsine Ltd.

1 Hanagar St. P.O.Box 7220 Hod Hasharon 45421 Israel

Tel: +972 9 7755100 Fax: +972 9 7755111 sales@powerdsine.com

#### **North America**

PowerDsine Inc. 290 BroadHollow Road Suite 305E Melville, NY 11747 Tel: +1 631 756 4680 Fax: +1 631 756 4691 sales@powerdsineusa.com

#### Europe

PowerDsine UK
Lakeside House
1 Furzeground Way
Stockley Park, Uxbridge
UB11 1BD, United Kingdom
Tel: +44 (0) 208 622 3107
Fax: +44 (0) 208 622 3200
uk@powerdsine.com

www.powerdsine.com



# 3. PD Characteristics Tests

Using the above setups, the PD's PoE characteristics are analyzed:

Test Item	Feature Comments	
PD Signature	802.3af	
PD Power Input	Data / Spare	
Voltage Polarity	Insensitive	
Power Class	0 *See Table 1	
Power consumption	3.6W	*Measured w/o load
Splitter	None	

Class	PD Power (W)
0	0.44 -12.95
1	0.44 - 3.84
2	3.84 - 6.49
3	6.49 -12.95
4	Future use

Table 1 - Power Classes

#### 4. Pass/Fail Tests

The following tests are performed using both PowerDsine's midspan and a PoE switch (PowerDsine inside).

Test Item	PD- Midspan	PoE Switch	Comments
On/Off test (1m cable)	Pass	Pass	
On/Off test (100m cable)	Pass	Pass	

# 5. Power Source Boundaries

The following measurement is done using midspan with an external variable power supply:

Max operating voltage	85V
Min operating voltage	33V

# 6. Summary and Conclusions

The PD passed all tests and is compatible with both PowerDsine's Midspan and PoE switch, based on PowerDsine's integrated solutions.

# The PD works with Resistor Detection, according to IEEE 802.3af standard.

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**International Headquarters North America** Europe www.powerdsine.com PowerDsine Ltd. PowerDsine Inc. PowerDsine UK 1 Hanagar St. 290 BroadHollow Road Lakeside House P.O.Box 7220 Suite 305E 1 Furzeground Way Stockley Park, Uxbridge Hod Hasharon 45421 Melville, NY 11747 Tel: +1 631 756 4680 UB11 1BD, United Kingdom Tel: +972 9 7755100 Fax: +1 631 756 4691 Tel: +44 (0) 208 622 3107 Fax: +972 9 7755111 sales@powerdsineusa.com Fax: +44 (0) 208 622 3200 sales@powerdsine.com uk@powerdsine.com







Validity of the above tests is based on the presumption that the PD is IEEE 802.3af compliant, as declared by its vendor.

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For more information, please contact PowerDsine technical support:

E-mail: Customer.Care@PowerDsine.com

Tel: +972-9-7755123

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PowerDsine Ltd. 1 Hanagar St. P.O.Box 7220 Hod Hasharon 45421

Israel

Tel: +972 9 7755100 Fax: +972 9 7755111 sales@powerdsine.com

#### North America

PowerDsine Inc. 290 BroadHollow Road Suite 305E Melville, NY 11747 Tel: +1 631 756 4680 Fax: +1 631 756 4691 sales@powerdsineusa.com

#### Europe

PowerDsine UK Lakeside House 1 Furzeground Way Stockley Park, Uxbridge UB11 1BD, United Kingdom Tel: +44 (0) 208 622 3107 Fax: +44 (0) 208 622 3200 uk@powerdsine.com

www.powerdsine.com